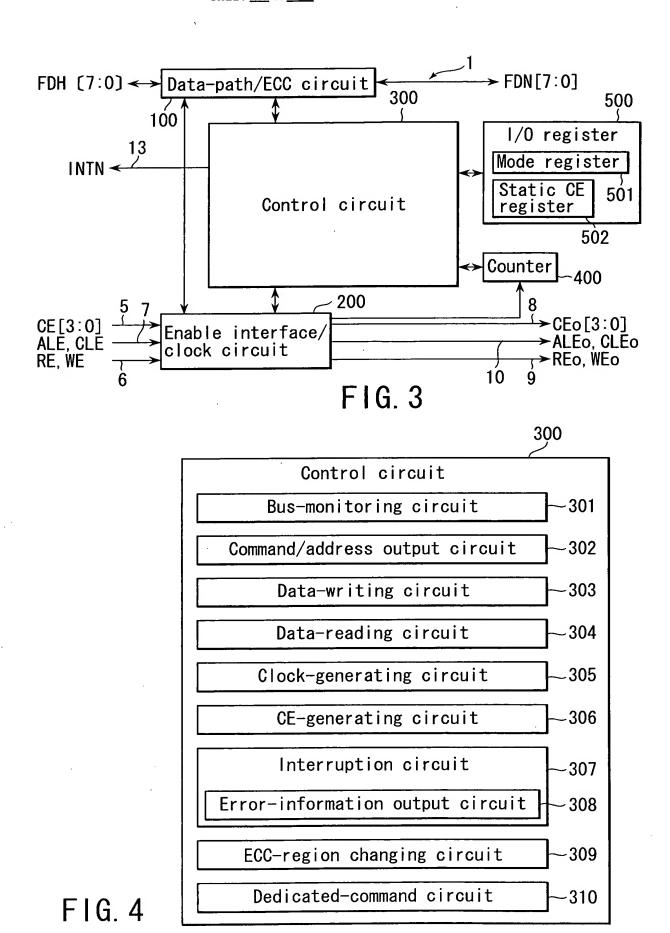


To the ECC controller FDN[7:0] [0] [1] [2] [3] 10 **NAND NAND** NAND **NAND** flash flash flash flash memory memory memory memory [\]2A ^l2B l 2C 2D J 11 12 FIG. 2



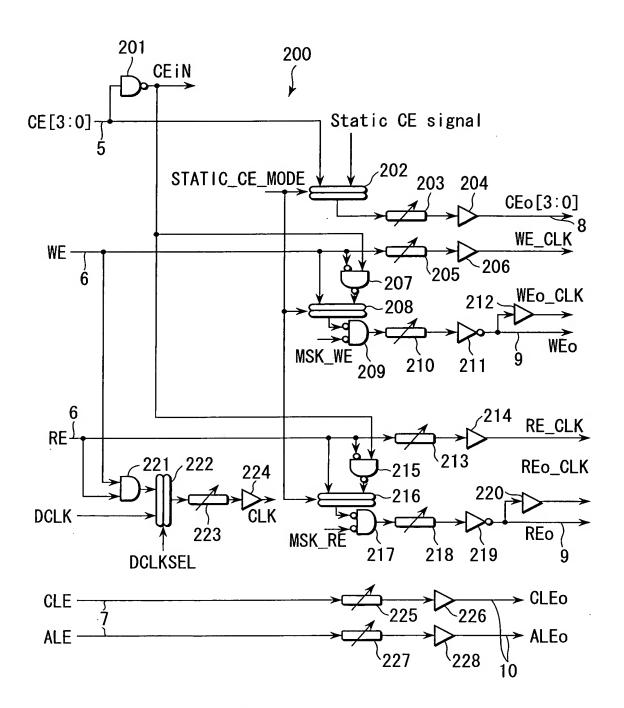
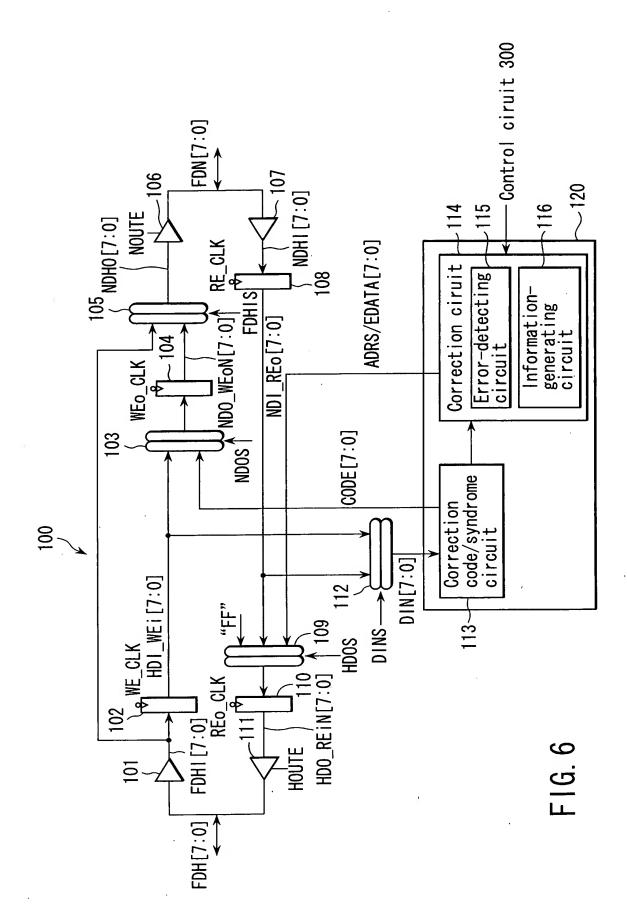


FIG. 5



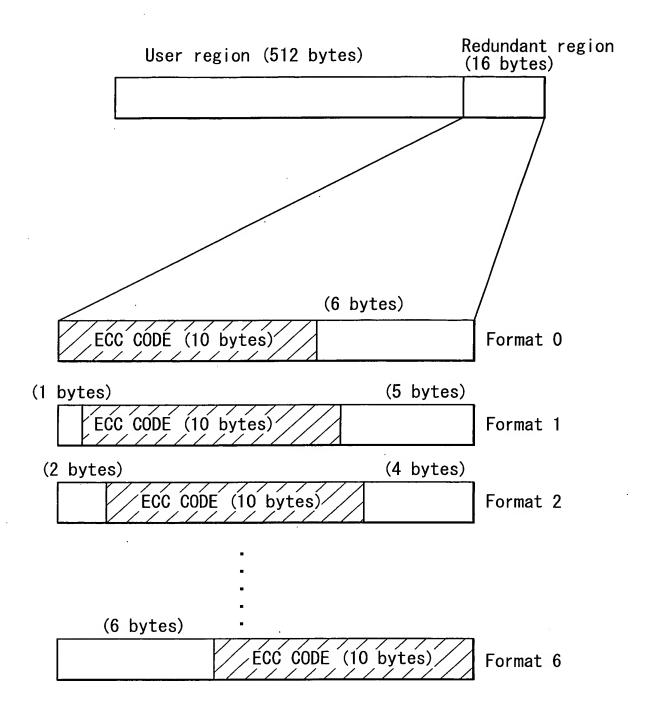
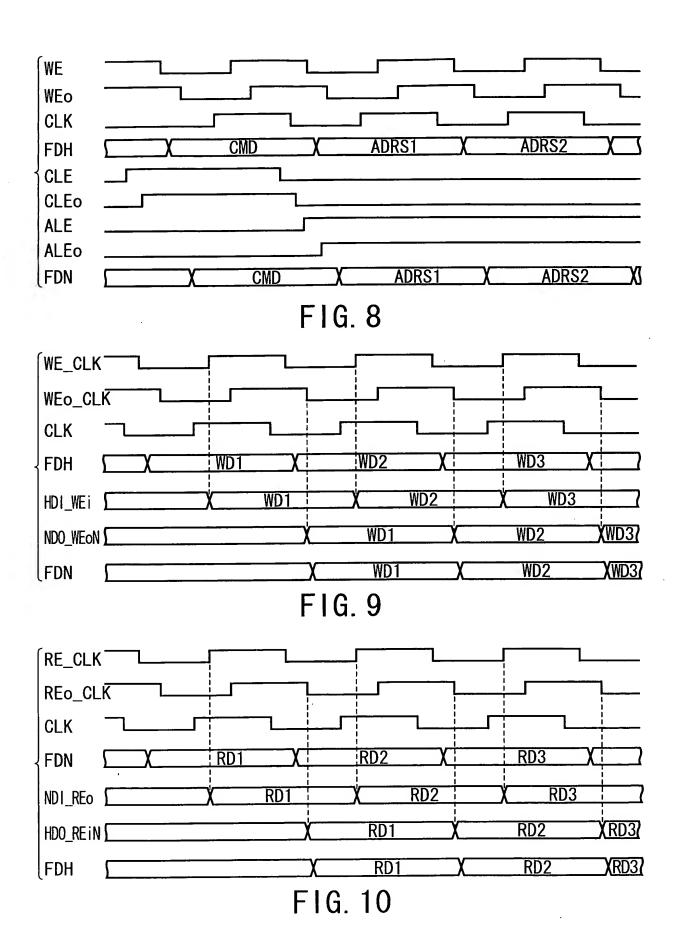


FIG. 7



s

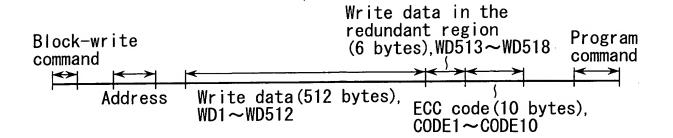


FIG. 11

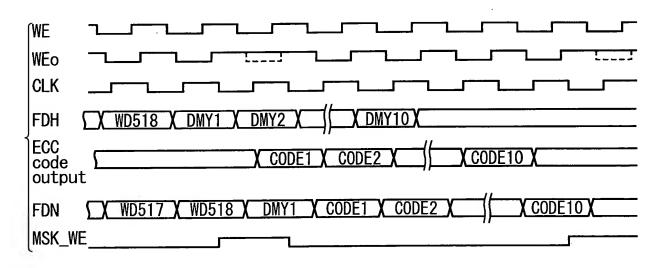


FIG. 12

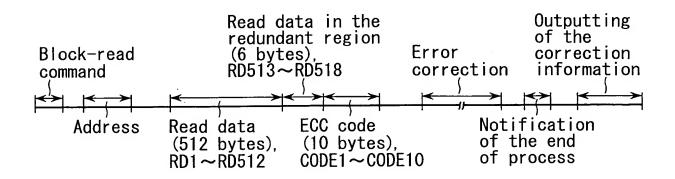


FIG. 13